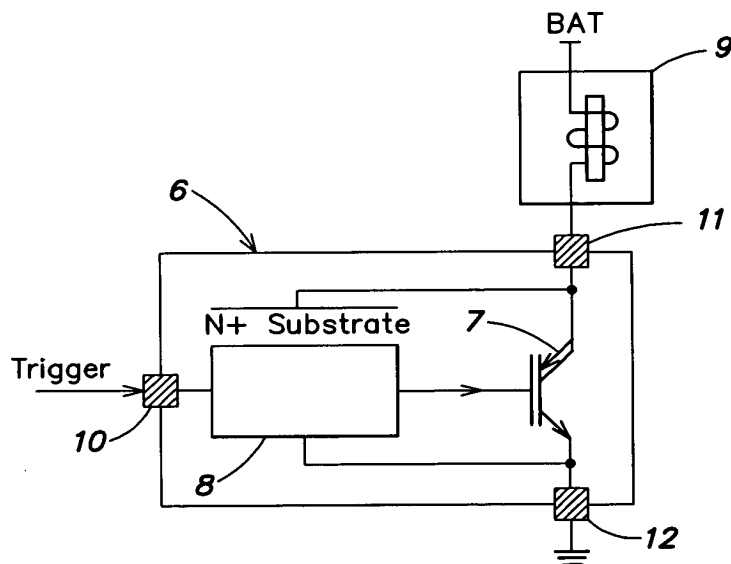
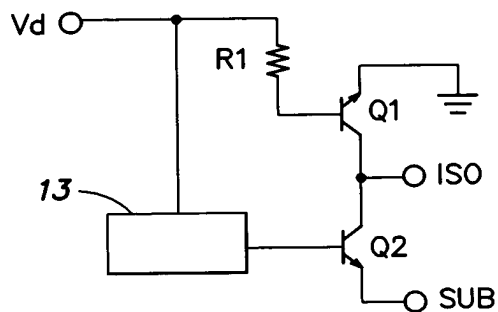


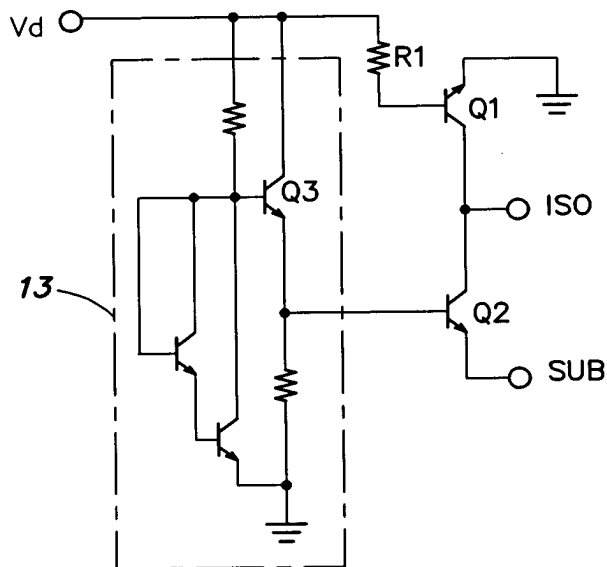
**FIG. 1**  
(Prior Art)



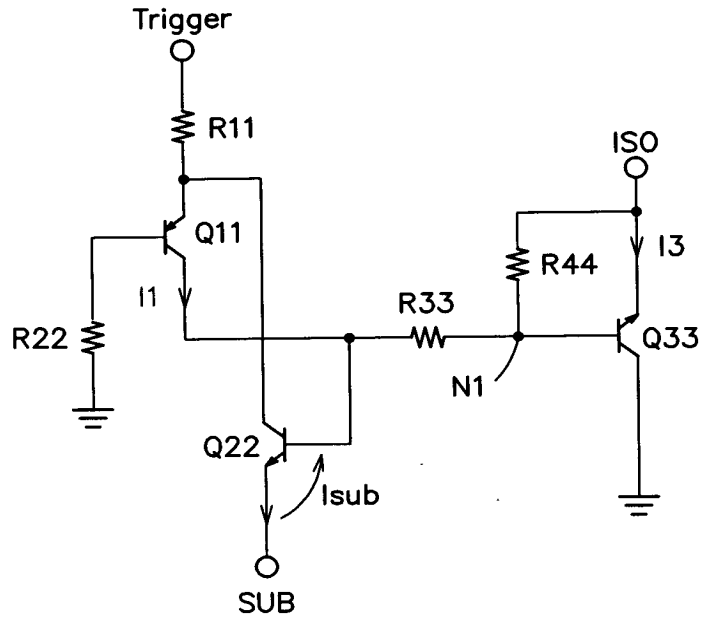
**FIG. 2**  
(Prior Art)



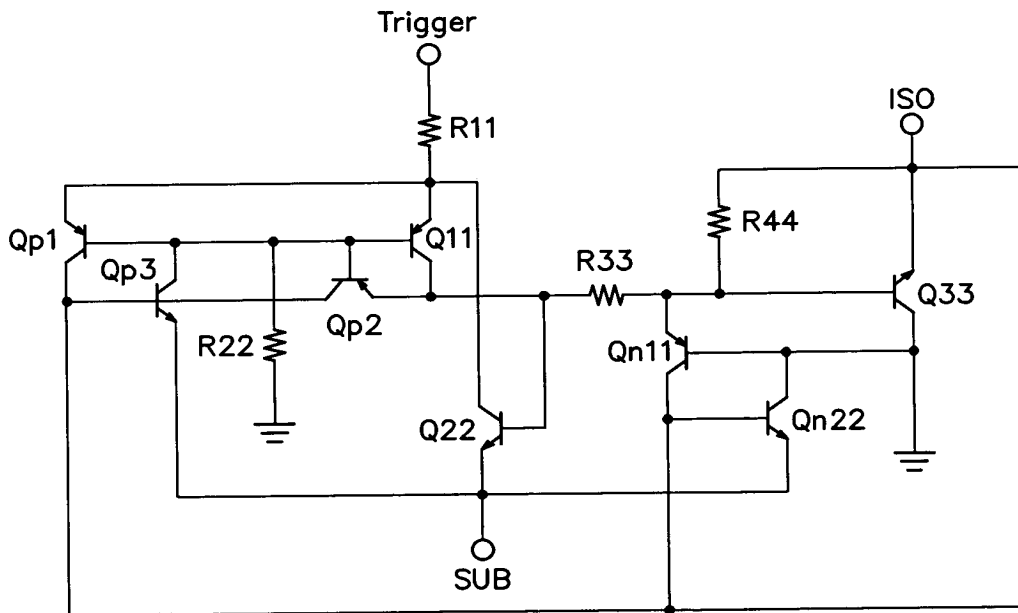
**FIG. 3**  
(Prior Art)



**FIG. 4**  
(Prior Art)



**FIG. 5**



**FIG. 6**



Serial No: 09/497,916  
 For: AN INTEGRATED CIRCUIT INCLUDING PROTECTION AGAINST  
 POLARITY INVERSION OF THE SUBSTRATE POTENTIAL  
 Antonino Torres and Sergio Tommaso Spampinato

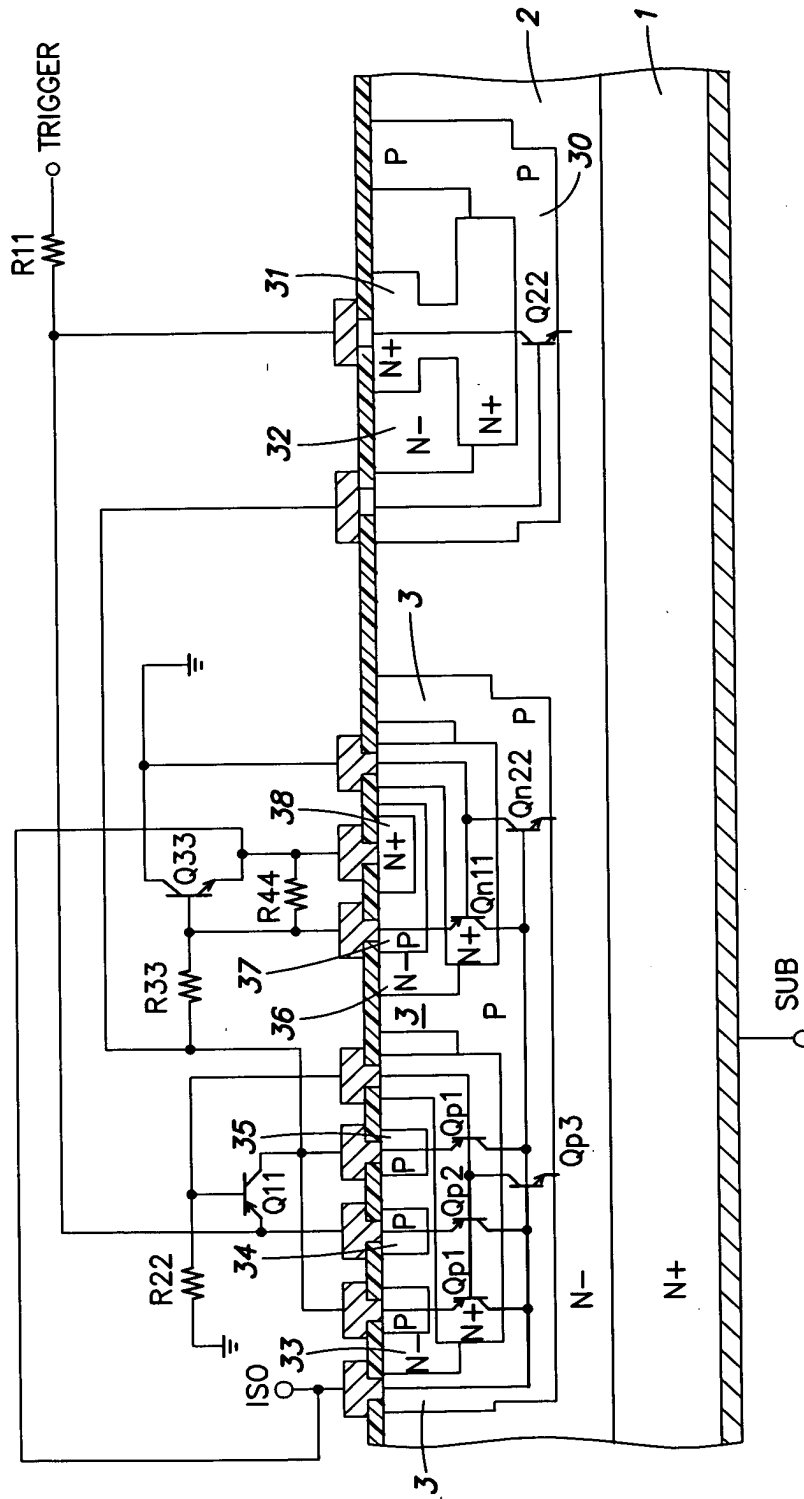


FIG. 7

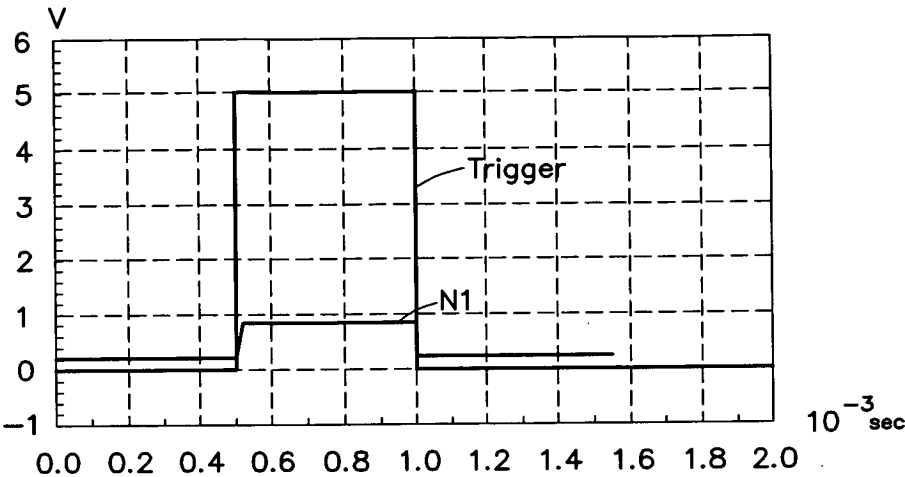


FIG. 8

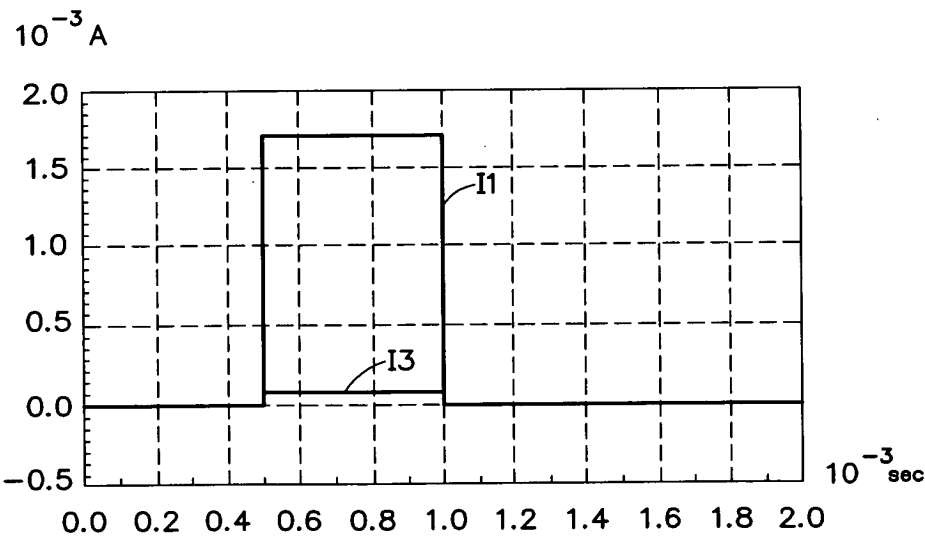
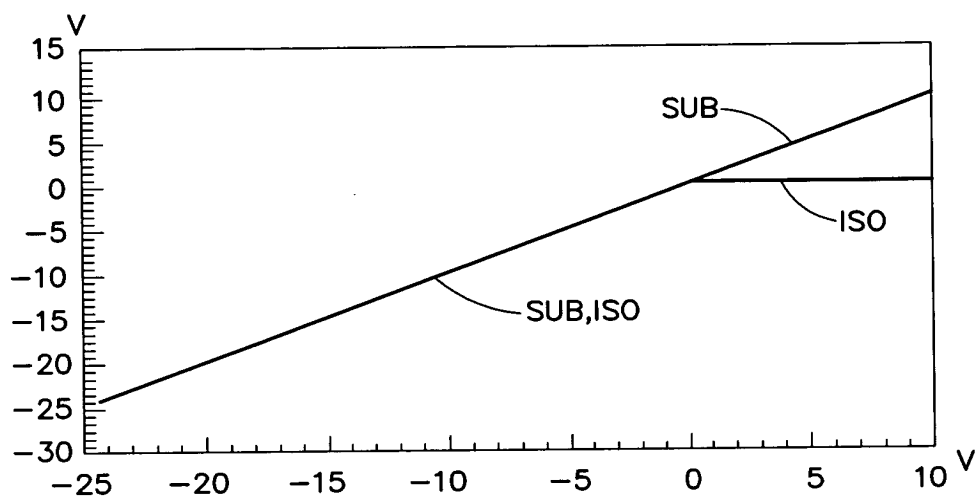


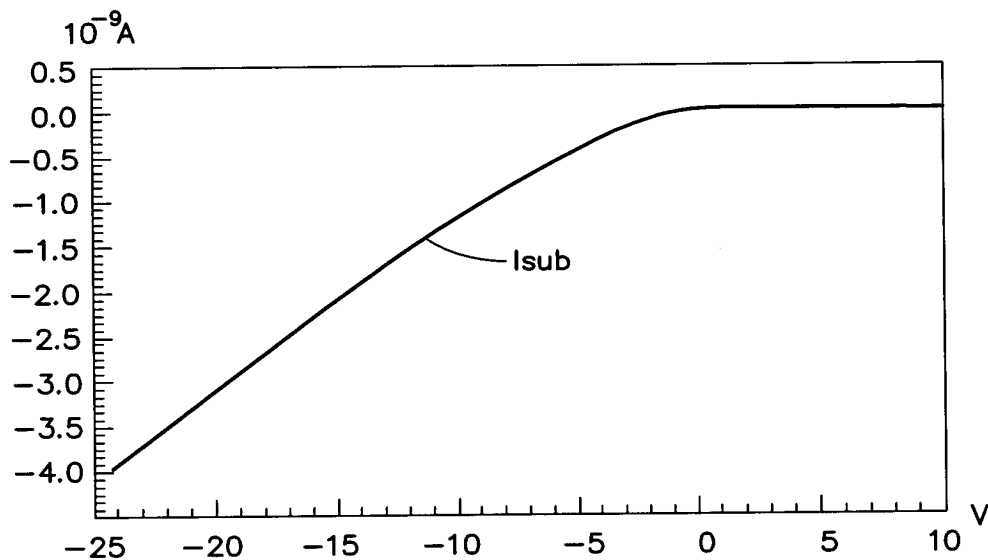
FIG. 9



Serial No: 09/497,916  
For: AN INTEGRATED CIRCUIT INCLUDING PROTECTION AGAINST  
POLARITY INVERSION OF THE SUBSTRATE POTENTIAL  
Antonino Torres and Sergio Tommaso Spampinato



**FIG. 10**



**FIG. 11**

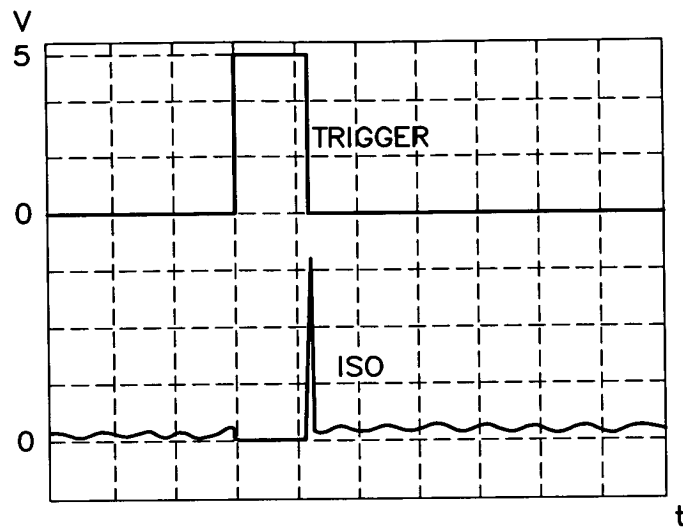


FIG. 12

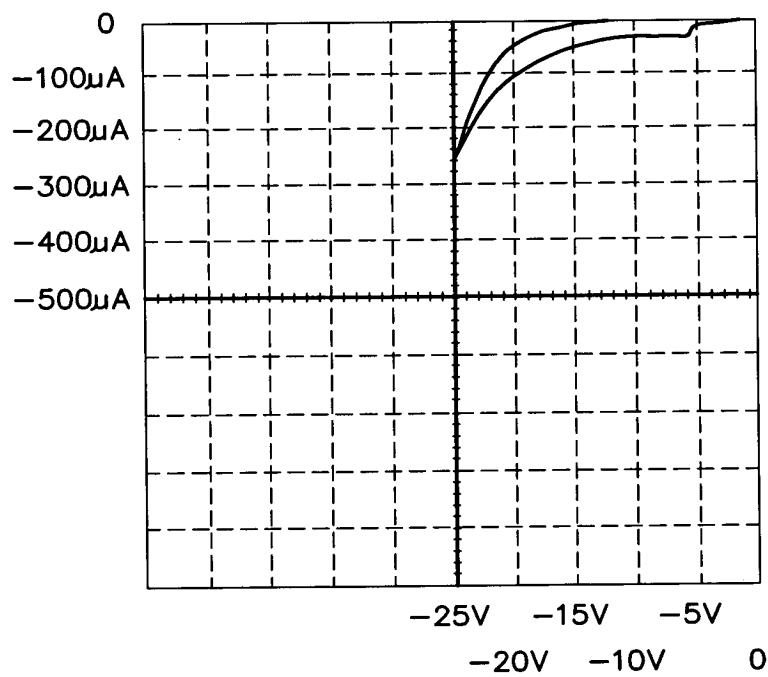


FIG. 13